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Previous work used speculative execution to leak information

This work:

Use speculative execution to hide arbitrary computation

- Useful for malware or white-box applications



Outline

- Background
 - Speculative Execution
 - Spectre / Meltdown
- Threat Model & Architecture
- Fundamental limits of speculative execution
 - How much work can be done?
 - What kinds of work can gadgets do?
- My processor can do what speculatively?!
 - Techniques for obfuscating program behavior
- System Implementation





Background: Speculative Execution & Spectre



Speculative Execution



...



Speculative Execution





Speculative Execution



Executes instructions inside branch without conditions applied.

Results are discarded once speculation resolves... right?



Spectre 1

Direct Jumps

1) Attacker **trains** branch predictor.

array[10] = "9876543210" secret[] = "P4ssw0rd1234"; if (i < 10) { a = array[i]; b = map[a];

Victim Process





Spectre 1

Direct Jumps

```
array[10] = "9876543210"
secret[] = "P4ssw0rd1234";
if (i < 10) {
    a = array[i];
    b = map[a];
}</pre>
```

1) Attacker **trains** branch predictor.

Victim Process

2) Attacker provides out of bounds index to the array.





Spectre 1

Direct Jumps

array[10] = "9876543210"
secret[] = "P4ssw0rd1234";
if (i < 10) {
 a = array[i];
 <u>b = map[a];</u>
}

1) Attacker **trains** branch predictor.

Victim Process

2) Attacker provides out of bounds index to the array.

3) Attacker exfiltrates sensitive info via **Side-channel**







Indirect Jumps



The branch predictor guesses **where** control flow will be redirected.



ExSpectre: Hiding Malware in Speculative Execution

Overview

Spectre / Meltdown use speculative execution to **Leak** information

This work will use speculative execution to **Hide** arbitrary malicious computation



Hiding Computation



Current Malware

- Packers
 - Dynamic analysis can undo packing
- Triggers / Red Pill
 - Static analysis can identify conditions and triggers

- Our Work: ExSpectre
 - Require analyst to **precisely model** speculative execution



ExSpectre Threat Model

Attacker Capabilities

- Install binary on target machine
- ✓ Influence trigger program
 - Possibly remotely

Reverse Engineer Capabilities

- Can use static and dynamic analysis.
- × Can't introspect processor's speculative state.
- X Can't run trigger program



Trigger – Trains branch predictor pattern – target_fn





 $\begin{array}{l} Trigger - {\sf Trains branch predictor} \\ {\sf pattern} \ \rightarrow \ target_fn \end{array}$

Payload

Executes same jump pattern





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 $\begin{array}{l} Trigger - {\sf Trains branch predictor} \\ {\sf pattern} \ \rightarrow \ target_fn \end{array}$

Payload

- · Executes same jump pattern
- CPU mis-speculates to target_fn





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 $\begin{array}{l} Trigger - {\sf Trains branch predictor} \\ {\sf pattern} \ \rightarrow \ target_fn \end{array}$

Payload

- Executes same jump pattern
- · CPU mis-speculates to target_fn
- · Executes a short gadget speculatively





Trigger – Trains branch predictor pattern \rightarrow target_fn

Payload

- · Executes same jump pattern
- · CPU mis-speculates to target_fn
- · Executes a short **gadget** speculatively
- · Results sent to real world via side channel





 $\begin{array}{l} Trigger - {\sf Trains branch predictor} \\ {\sf pattern} \ \rightarrow \ target_fn \end{array}$

Payload

- · Executes same jump pattern
- · CPU mis-speculates to target_fn
- · Executes a short **gadget** speculatively
- · Results sent to real world via side channel





How much work can be done speculatively?

















1) Different instructions have different limitations.

2) Simpler vs. more complex instructions.





The maximum number of instructions aligns with **ROB size** for the simplest instructions.







• Cache Limit – Cache miss duration

- Signal still detectable after 80% resolution (300 cycles)
 - No signal after 95% resolution (750 cycles)
- Using instructions with high CPI hit this limit first.

• ROB Limit – ROB size: 220 µops (Skylake)

We can execute ~100-150 instruction speculatively





Control flow, logical, & arithmetic instructions



Control flow, logical, & arithmetic instructions

AES-NI instructions



- Control flow, logical, & arithmetic instructions
- AES-NI instructions
- Load (in cache)



- Control flow, logical, & arithmetic instructions
- AES-NI instructions
- Load (in cache)
- Load (out of cache)



- Control flow, logical, & arithmetic instructions
- AES-NI instructions
- Load (in cache)
- Load (out of cache)

× Store



- Control flow, logical, & arithmetic instructions
- AES-NI instructions
- Load (in cache)
- Load (out of cache)
- × Store

× Syscalls







Emulator



Emulator

- Store
- Syscall
- · Maintains state
- · State accessible speculatively



How to train your Branch Predictor



Triggers — Spectre Variants

Spectre 2 Indirect Branches	Entry point determined by training in trigger process.
Returns	Entry point determined by training in trigger process.
Spectre 1.1 Direct Branches	Entry point determined by attacker controlled data.

Benign Triggers

The trigger program doesn't have to be complicit (e.g. – openssl)



Triggers — Nested Speculation

Processors continue speculate branches even while executing speculatively.



Tested successfully on Intel Core i5-7200U and Intel Xeon CPU E3-1270 v6



Payloads - Decryption Gadgets

AES-NI instructions

- Hardware supported AES Block
 Encryption and Decryption
- Abbreviated Key derivation (or partial key expansion)



Incrementally decrypt a data blob speculatively



Implementation: Putting it all together





Benign Remote Trigger





Benign Remote Trigger
 Decryption Gadget





- Benign Remote Trigger
- Decryption Gadget
- **Custom Emulator**





- · Benign Remote Trigger
- Decryption Gadget

Custom Emulator

Quickly launch **reverse shell** once trigger becomes present



Analysis without Trigger



Emulator Exists
 Encrypted Binary
 Cache Probe

X GadgetsX Entry Point



Defenses & Analysis



Insufficient Defenses

Spectre Defenses:

IBPB - Predictor state optionally cleared on context switch

IBRS - Predictor cleared on kernel enter/exit

<u>STIBP</u> - Different predictor per hyperthread

<u>Retpoline</u> - Software patch for Spectre II (opt-in)

Cache Coloring - but still other side channels

...but most are *opt-in!*

Attacker can choose no defenses



We use speculative execution to:

• Hide core malware functionality

- Difficult for static/dynamic analysis to reverse engineer
- Implemented **reverse shell** with support of a small emulator
- Triggered by
 - Other potentially benign / remote programs
 - Input data









ExSpectre: Hiding Malware in Speculative Execution